

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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10/613,192

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Applicant:

Inao Toyoda, et al.

Group Art Unit:

2812

Examiner:

H. Jey Tsai

Title:

PHOTO SENSITIVE INTEGRATED CIRCUIT DEVICE

AND RELATED CIRCUIT ADJUSTMENT

Attorney Docket:

4041P-000005/DVA

Director of The United States Patent and Trademark Office P.O. Box 1450 Alexandria, Virginia 22313-1450

RESPONSE

Sir:

In response to the Notice Regarding Drawings mailed July 8, 2005, enclosed is a replacement sheet for Figure 8. Figure 8 has been amended by moving the information which was cut off on the right side of the sheet to a position below the graph.

Applicants respectfully request the entrance of this amended drawing and issuance of the patent for this application.

Respectfully submitted,

Date: <u>July 20, 2005</u>

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MJS/pmg